

PERSONAL INFORMATION	I was born in May 26th, 1988 in Banská Bystrica, Slovakia and grew up in Napajedla, Czech Republic. I lived, studied and worked in Prague from 2007 to 2016. I am currently a software engineer at Red Hat.	
CONTACT	Mobile phone: +420 736 452 265 E-mail: roman.pavelka.asi@gmail.com Web: romanpavelka.cz Github: ChaoticRoman	
EDUCATION	Master programme Engineering informatics Terminated without degree Faculty of Nuclear Sciences and Physical Engineering Czech Technical University in Prague	2011 - 2014
	Bachelor degree in Engineering informatics Faculty of Nuclear Sciences and Physical Engineering Czech Technical University in Prague Thesis: <i>Design and Construction of Fluxgate Magnetometer for Space Research</i>	2007 - 2011
	Gymnázium Zlín, Lesní čtvrť	2003 - 2007
EMPLOYMENT	Associate software engineer Duties: <i>Python development, process and test automation, tech lead of small DevOps team</i> Red Hat Czech	2016 - present
	Freelance hardware, firmware and software developer <i>Application design, embedded development in C for 8-bit AVR, SW development in Python</i>	2014 - present
	Technician Duties: <i>Embedded SW development, testing and verification for Lunar Plasma Monitor</i> Astronomical Institute Academy of Sciences of the Czech Republic	2010 - 2014
	Mathematician Duties: <i>development and maintenance of scientific data archive for instrument DSLP on PROBA2</i> Institute of Atmospheric Physics Academy of Sciences of the Czech Republic	2011 - 2014
SKILLS	<b>Computer</b> Programming languages: Python (2.7, 3.5, 3.6), C (ANSI C, C99), C++11, Bash GNU/Linux: advanced user, basic system administration Networking: TCP/IP, SSH, SCP, rsync, nmap, VPS deployment, CMS administration Version control systems: Git, Subversion  <b>Electronics</b> Analog electronics: linear and switching power supplies, measurement circuits Digital electronics: Atmel AVR 8-bit microcontrollers, Raspberry Pi, basic digital circuits Interfacing: ADCs, DACs, user interface devices, RS-232, SPI, 1-wire Other: circuit design in KiCad, single and double-side PCBs manufacturing, soldering  <b>The European Cooperation for Space Standardization (ECSS)</b> Engineering standards: System Engineering, Technical Requirements Specification, Verification, Testing  <b>Languages</b> Czech (native), English (fluent), German (basics), Spanish (basics)	
STUDY INTERESTS	software architecture, artificial intelligence, probability and statistics, software testing	
PERSONAL INTERESTS	open source hardware and software, teaching, traveling, percussion instruments playing	